



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re New Patent Application of)
Shunpei YAMAZAKI)
Serial No. Not Yet Assigned) Attn: Applications
Filed: October 5, 1999) Branch
For: SEMICONDUCTOR DEVICE INCLUDING)
SEMICONDUCTOR CIRCUIT MADE FROM)
SEMICONDUCTOR ELEMENT AND)
MANUFACTURING METHOD THEREOF) Date: October 5, 1999

#5
IOS
3/22/00

INFORMATION DISCLOSURE STATEMENT
AND NOTIFICATION OF RELATED APPLICATIONS

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:


In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

The Examiner's attention is directed to U.S. Serial Nos. 08/633,307 and 08/928,514. These applications are in the family of Japanese Patent Laid-Open

No. 07-130652, along with U.S. Patents 5,643,826 and 5,923,962. Also, the Examiner's attention is directed to U.S. Serial No. 08/785,489, which is in the family of Japanese Laid-Open No. 09-312260.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Respectfully submitted,



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